

Listing of Claims

This listing of claims will replace all prior versions and listings of claims in the Application.

1-31. (CANCELED)

32. (CURRENTLY AMENDED) A semiconductor surface free of interfacial reactions between the surface and a second molecular species comprising:

a semiconductor surface with one atomic layer of valence-mending atoms and a second molecular species, wherein the valence-mending atoms comprise atoms that create a second surface on the one atomic layer and in contact with the second molecular species, the second surface without dangling bonds and free of interfacial reactants ~~on the valence-mending occurs after introducing the semiconductor surface to a passivating agent.~~

33. (CURRENTLY AMENDED) The semiconductor surface of claim 32, wherein the second molecular species is selected from the group consisting of metals, dielectrics, dielectric precursors, metal oxides, oxygen, water vapor, carbon, hydrogen, carbon dioxide, carbon monoxide, and combinations thereof.

34. (ORIGINAL) The semiconductor surface of claim 32, wherein the semiconductor surface is selected from the group consisting of silicon, germanium, silicon-germanium and silicon-carbide.

35. (CURRENTLY AMENDED) The semiconductor surface of claim 32, wherein the valence mending atoms are from passivating the semiconductor surface with a passivating agent, the passivating agent is selected from the group consisting of Group V, VI, or VII cogener, ~~or~~ and hydrogen.

36. (ORIGINAL) The semiconductor surface of claim 32, wherein interfacial reactions are selected from the group consisting of oxidation, chemical adsorption, solicidation, and combinations, thereof.

37. (WITHDRAWN) A kit for preventing interfacial reactions from occurring on a semiconductor surface comprising:

a passivating agent; and

an instructional manual.

38. (WITHDRAWN) The kit of claim 37, wherein the instructional manual is selected from the group consisting of a computer disk, CD-ROM, electronic media, brochure, and combinations thereof.

39. (WITHDRAWN) The kit of claim 37, wherein the passivating agent is selected from the group consistency of Group V, VI, or VII cogener, or hydrogen.

40. (NEW) The semiconductor surface of claim 35, wherein the passivating agent is selected from the group consisting of a Group V, VI, or VII cogener and hydrogen.

41. (NEW) The semiconductor surface of claim 35, wherein the passivating agent is temperature sensitive.

42. (NEW) The semiconductor surface of claim 41, wherein a temperature below 700 degrees Centigrade prevents interfacial reactions.

43. (NEW) The semiconductor surface of claim 33, wherein the dielectric and dielectric precursor is a high dielectric constant material with a dielectric constant larger than 4.